

Appln. of: STEWART et al.
Serial No.: 09/727,466
Filed: December 4, 2000

Amendments to the Specification

Please replace the paragraph beginning on line 8 of page 1 with the following paragraph:

This application is a continuation-in-part of patent application number 09/294,591, filed 04/20/99 (~~pending~~), now US patent number 6,377,629, which is a continuation-in-part of patent application number 08/744,629, filed 11/06/96, now US patent number 5,926,509, which is a continuation-in-part of patent application number 08/741,697, filed 10/31/96 (~~pending~~), now US patent number 6,150,997, which is a continuation-in-part of patent application number 08/219,979, filed 03/29/94, now US patent number 5,576,723. Patent application number 08/744,629 also claims the benefit of provisional patent application number 60/010,741, filed 01/29/96. The instant application is also a continuation-in-part of patent application number ~~08/66,076~~ 08/660,076, filed 06/03/96 (~~pending~~), now US patent number 6,184,919, which is a continuation-in-part of patent application number 08/177,442, filed 01/05/94, now abandoned.

Please replace the paragraph beginning on line 1 of page 14 with the following paragraph:

Referring now to Fig. 1a and Fig. 1b, there is shown a largely schematic electrical diagram of another embodiment of the invention wherein the plurality of computers as shown in Fig. 1 each provides a set of red (SR), green (SG) and blue (SB) color video signals. A switching circuit, such as the aforementioned Commander(tm) module, serves as a source of the signals, providing a selected set of red (SR), green (SG) and blue (SB) color video signals to respective transmitters, each similar to the circuitry as shown in

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Fig. 1, as should be apparent to one skilled in the art. Vertical (SV) and horizontal ~~(Sn)~~
(SH) synchronization signals are also transmitted, as will be described.

**Please replace the paragraph beginning on line 2 of page 22 with the
following paragraph:**

Referring further to Fig. 5, an input signal to signal pair 80 of delay unit 64, as from cross-switcher 73 (Fig. 1b), connects to terminals 82 and 84, one of them, terminal 82, being attached to a conductor of pair 80 on the reverse side of board ~~64~~ 70, and terminal 84 being connected to the other conductor of pair 80 on the top side of board 70.

Similarly, a signal output line 83, to one of receivers 74 or 76, would have its conductors connected to conductor terminal 81 on the top side of board ~~64~~ 70, and terminal 85 on the bottom side of board 70.